Link_A_Media Devices (LAMD) is the new leader in developing and manufacturing custom System-on-Chip (SOC) solutions for peripheral data storage devices, which include hard disk drives (HDDs) and solid-state drives (SSDs). The SoC solution integrates the Read/Write Channel, disk controller functions, microprocessor(s) and memories, and servo processing for HDD application on a single chip.

Link_A_Media Devices provides the most leading-edge and advanced infrastructure and support for developing and manufacturing such SoC chips, including:

- Leading-edge CMOS process technologies
- State of the art cache memory technology
- Proven design flow and methodology
- Unmatched DFM flow and techniques
- High-speed/Low-power open microprocessor sub-systems
- New class of powerful data recovery methods
- IP blocks to support standard host interfaces
- Custom and semi-custom physical design
- Worldwide delivery logistics

With its unmatched data recovery technologies, Link_A_Media Devices’ custom SoC solutions deliver the highest areal densities, highest drive yields, and lowest power consumption. Those advantages enable hard disk drive (HDD) and solid-state drive (SSD) OEMs to continue to meet the growing demand for fast and low-cost data storage worldwide in consumer, computing, and storage networking applications.

**CAREER OPPORTUNITIES:** jobs@link-a-media.com

- **ASIC Design & Verification Engineers**
- **Application Engineers**
- **Analog Design Engineers**
- **Firmware/Software Engineers**

**Analog Design Engineer**

- Experience of modeling hi-speed I/O’s, PCB interconnects, and package with respect to parameters such as jitter, reflection, crosstalk, SSN and ground/power bounce.
- Have strong timing and signal integrity background of DDR2/DDR3/Flash memory interface system.
- Have strong knowledge of transmission lines and low-power termination schemes.
- Capable to provide design guidelines to PCB layout and perform post route simulations for timing and signal quality.
- Hands on DDR2/3 IO and associated control circuitry design experience with design tools including HSPICE and Cadence Spectre and familiar with physical layout and post-layout simulation.
- Familiarity with IBIS model is a plus.

**Requirements:**

- MSEE with 5+ years of experience in transistor level circuit design.
- Familiar with lab equipment such as oscilloscope.
- Excellent communication skills and ability to interface with other teams and customers in a dynamic start-up environment.

**Jr. & Sr. Level Firmware/Software Engineers**

**Requirements:**

- BS in either Computer Science or Electrical Engineering; MS is preferred
- Perform system integration, validation and debugging of FPGA and SoC design
- Develop new test software and utility tools to validate SSDs compatibility and reliability for target applications
- Setup the SATA storage test systems and prepare test scripts for SSD evaluation
- Perform failure analysis by using storage interface bus analyzer and digital scope, identifying the issues, duplicating the problems, and resolving the issues with corrective actions
- Proficient in structured firmware and software programming (C/C++, assembly, script languages)
- Hands-on experience in verification and trouble shooting in an embedded firmware application preferable with ARM based development tools
- Good working knowledge of design in digital logic, FPGA and SoC
- Experience of firmware debugging tools such as JTAG, In-Circuit Emulator, scopes and logic/bus analyzer
- Ability to work creatively and analytically in a problem-solving environment
- Good oral and written skills for communication and documentation
- Open to domestic and/or international travel for technical consultation
- Self-motivated, strong team player
Jr. Level Requirements:
- Design, implement and evaluate real-time embedded system firmware for Serial ATA (SATA) & PCIe SSD controllers that optimize data access & cache performance, endurance & reliability for high performance desktop & enterprise class SSD products
- Work with major OEM customers to implement SSD functions per customer reqs, debug customer issues and prepare test report
- 6 month of development experience in team-based, complex programming in the embedded controller environment with RTOS
- Knowledge of mass storage systems: NAND flash memory, hard disk drives, SATA/ATA/PCIe, data caching and device drivers; Experience in the NAND flash storage device application and/or testing is a plus

Sr. Level Requirements:
- Design, implement and evaluate real-time embedded system firmware for Serial ATA (SATA) and Serial Attached SCSI (SAS) SSD controllers that will optimize performance, endurance and reliability for high performance desktop and enterprise class SSD products
- Work with major OEM customers to implement SATA SSD functions per customer requirements, debug customer issues and prepare test report
- 10+ years of development experience in team-based, complex programming in the embedded controller environment with Real Time Operating System (RTOS)
- 7+ years of firmware development experience in mass storage systems: solid state drives, hard disk drives, SATA/ATA storage devices, data caching or device drivers; Experience in the NAND flash storage device application and/or testing is a plus
- 5+ years of technical management experience
- A high level of skill in identifying performance critical algorithms for peripheral storage system optimization

SSD System Validation Engineer

Job description:
- Develop infrastructure and tools to validate SSD products with different storage interfaces like SATA, PCIe, eMMC.
- Firmware integration and debugging.
- Performance testing and benchmarking of SSDs.
- Develop failure analysis tools that can root cause the failures.

Requirements:
- Experience in ARM system debugging.
- Proficient in embedded software programming.
- Knowledge about host interfaces like PCIE, SATA.
- Prior experience in storage of driver development for windows/linux platforms is a plus.

Senior ASIC Design Engineers

Job description:
- The qualified design applicant would be responsible for all aspects of the design activities, including architecture definition, design specification, design flow development, logic design and verification, timing closure, test vector generation, etc. for new generation SSD (Solid State Drive) controller ICs.

Requirements:
- MS with 5 years or BS with 7 years in ASIC design
- Hands-on experience in all aspects of RTL design flow from specification/architecture definition to design and verification
- Strong abilities in HDL design and modeling language
- Familiarity with digital signal processing, SAS, SATA, PCIe, DDR is a plus
- System level / chip level experience and ECC experience is a plus
- SSD, Flash controller, Nand management IC design experience is desirable
- Experience with FPGA emulation and hardware validation is desirable
- Being through iterations of design cycles including production is desirable
- VCS, NC verilog, Formality, DCT, RTL-compiler, PrimeTime etc. is desirable
- Background in verification methodologies including random and coverage driven verification is a plus

Staff Verification Engineer

Requirements:
- 4 - 8 years working experience in SoC verification
- familiar with the digital design flow, including synthesis, timing closure, and formal check
- experienced in C/C++, Verilog/SystemVerilog, Perl
- familiar with the latest verification methodologies such as VMM, OVM, and UVM
- experienced in building BFM and C++ models of various SoC blocks
- experienced in using code coverage, functional coverage, and assertions
- Knowledge in AHB, SATA, PCIe protocols.

Senior Verification Engineer

Requirements:
- 0 - 2 years working experience
- Understand digital design concepts
- Experience in C/C++, Verilog, Perl. SystemVerilog is a plus
- Familiar with OOB concepts
- Understands how to build testbench and testcases